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PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

RAGHUNATHAN, Anand, et al.

Appln. No.: 09/576,956

Filed: May 24, 2000

For: A METHODOLOGY FOR THE DESIGN OF HIGH-PERFORMANCE
COMMUNICATION ARCHITECTURES FOR SYSTEM-ON-CHIPS USING
COMMUNICATION ARCHITECTURE TUNERS



Group Art Unit: 2768

Examiner: Not yet assigned

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PRELIMINARY AMENDMENT

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Prior to examination, please amend the above-identified application as follows:

IN THE SPECIFICATION:

Page 1, please delete the second full paragraph, and replace it with the following
new paragraph:

A1
This disclosure teaches a methodology for the design of custom system-on-chips
communication architecture. Specifically a novel electronic system and a method of designing a
communication architecture are disclosed. This Application is currently filed with US Patent
Application No. 09/576,955 by Raghunathan et, al (Attorney Docket No. A7679).

update